

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) : Derick G. Behrends et al.
Serial No. : 10/636,060
Filed : August 7, 2003
For : METHODS AND APPARATUS FOR TESTING
INTEGRATED CIRCUITS
Examiner : James C. Kerveros
Group Art Unit : 2117
Customer No. : 46628

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT UNDER 37 C.F.R. 1.312

Sir:

In response to the Notice of Allowance and Issue Fee
Due mailed on April 21, 2008, Applicants submit this Rule 312
Amendment.

Amendments to the Claims begin on page 2

Remarks begin on page 6